

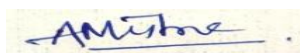
ACADEMIC LESSON PLAN OF WINTER 2026

Discipline: ETC	Semester:5 th	Name of the Teaching Faculty: ABINASH MISHRA
Subject: TH:1- DATA COMMUNICATION & COMPUTER NETWORKS	No. of days/per week class allotted:3p/week	Semester from: 01.07.2026 to 05.11.2026 No. of weeks:15 weeks
Week	Class/week	Theory Topics
1 st	1 st	Network & Protocol 1.1 Data Communication
	2 nd	1.2 Networks
	3 rd	1.2 Networks
2 nd	1 st	1.3 Protocol & Architecture, Standards- OSI, TCP/IP
	2 nd	1.3 Protocol & Architecture, Standards- OSI, TCP/IP
	3 rd	Data Transmission & Media 2.1 Data transmission Concepts and Terminology
3 rd	1 st	2.2 Channel capacity
	2 nd	2.3 Transmission media
	3 rd	2.4 Guided Transmission
4 th	1 st	2.4 Guided Transmission
	2 nd	2.6 Wireless Transmission
	3 rd	2.6 Wireless Transmission
5 th	1 st	Data Encoding 3.1 Data encoding
	2 nd	3.2 Digital data digital signals
	3 rd	3.2 Digital data digital signals
6 th	1 st	3.3 Digital data analog signals
	2 nd	3.4 Analog data digital signals
	3 rd	3.4 Analog data digital signals
7 th	1 st	3.5 Analog data analog signals
	2 nd	Data Communication & Data link control 4.1 Asynchronous and Synchronous Transmission
	3 rd	4.1 Error Detection
8 th	1 st	4.3 Line configuration
	2 nd	4.4 Flow Control
	3 rd	4.5 Error Control
9 th	1 st	4.6 Multiplexing
	2 nd	4.7 FDM synchronous TDM, 4.8 Statistical TDM
	3 rd	Switching & Routing 5.1 Circuit Switching networks
10 th	1 st	5.2 Packet Switching principles
	2 nd	5.3 X.25
	3 rd	5.4 Routing in Packet switching
11 th	1 st	5.5 Congestion
	2 nd	5.6 Effects of congestion, congestion control
	3 rd	5.7 Traffic Management
12 th	1 st	LAN Technology 6.1 Topology and Transmission Media
	2 nd	6.2 LAN protocol architecture
	3 rd	6.4 Bridges, Hub & Switch
13 th	1 st	6.4 Bridges, Hub & Switch

ACADEMIC LESSON PLAN OF WINTER 2026

	2 nd	6.5 Ethernet (CSMA/CD)
	3 rd	6.6 Wireless LAN Technology
14 th	1 st	6.6 Wireless LAN Technology
	2 nd	TCP/IP 7.1 TCP/IP Protocol Suite
	3 rd	7.2 Basic Protocol functions
15 th	1 st	7.3 Principles of Internetworking
	2 nd	7.3 Internet Protocol operations
	3 rd	7.4 Internet Protocol

CO No.	Course Outcome
CO1	Explain the fundamentals of data communication, networking concepts, communication protocols, and the OSI and TCP/IP reference models.
CO2	Analyze data transmission techniques, channel capacity, and the characteristics of guided and unguided transmission media used in computer networks.
CO3	Describe data encoding methods, error detection and correction techniques, flow control, multiplexing, and data link control mechanisms for reliable communication.
CO4	Compare switching techniques (circuit and packet switching), routing principles, congestion control methods, and traffic management strategies in communication networks.
CO5	Explain LAN technologies, Ethernet (CSMA/CD), networking devices (Hub, Bridge, Switch), wireless LAN concepts, and the architecture and operation of the TCP/IP protocol suite for internetworking.



Signature of Teaching Faculty

ACADEMIC LESSON PLAN OF WINTER 2026

Discipline: ETC	Semester: 3 rd	Name of the Teaching Faculty: ABINASH MISHRA
Subject: ELECTRONICS DEVICES	No. of days/per week class allotted: 3p/week	Semester from: 01.07.2026 to 05.11.2026 No. of weeks: 15 weeks
Week	Class/week	Theory Topics
1 st	1 st	Introduction to Semiconductor Physics 1.1 Review of Quantum Mechanics 1.2 Electrons in periodic Lattices
	2 nd	1.3 Energy bands in intrinsic and extrinsic silicon 1.4 Carrier transport 1.4.1 Diffusion current
	3 rd	1.4.2 Drift current 1.4.3 Mobility and resistivity
2 nd	1 st	P-N Junction Diodes 2.1 Generation and recombination of carriers 2.2 Poisson and continuity equation
	2 nd	2.3 P-N Junction Diodes 2.3.1 Construction of P-N Junction Diode
	3 rd	2.3.2 Operating Principle 2.3.3 P-N junction characteristics
3 rd	1 st	2.3.4 I-V characteristics
	2 nd	2.3.5 Small signal switching models
	3 rd	2.3.6 Avalanche breakdown
4 th	1 st	2.3.7 Zener diode
	2 nd	2.3.8 Schottky diode
	3 rd	2.3.9 LED
5 th	1 st	2.3.10 Photodiode and solar cell
	2 nd	Bipolar Junction Transistor (BJT) 3.1 Construction of BJT 3.2 Operating Principle of BJT
	3 rd	3.3 Types of BJT
6 th	1 st	3.5 I-V characteristics 3.4 Working principle of p-n-p and n-p-n BJT
	2 nd	3.6 Ebers Moll Model
	3 rd	3.7 Different types of transistor connection 3.7.1 Common Base (CB) 3.7.2 Common Emitter (CE) 3.7.3 Common Collector (CC)
7 th	1 st	3.8 Input and output characteristics of transistor in different connections
	2 nd	3.9 Define ALPHA, BETA and GAMMA of transistors in various modes.
	3 rd	3.10 Establish the Mathematical relationship between ALPHA, BETA and GAMMA
8 th	1 st	3.11 Basic concept of Biasing 3.12 Types of Biasing
	2 nd	3.13 h-parameter model of BJT 3.14 Load line and determine the Q-point. 3.15 Types of Coupling
	3 rd	3.16 Working principle and use of R-C Coupled Amplifier 3.17 Frequency Responses of R-C coupled Amplifier
9 th	1 st	FIELD EFFECT TRANSISTOR (FET) 4.1 FET & its classifications
	2 nd	4.2 Differentiate between JFET & BJT
	3 rd	4.3 Construction, working principle & characteristics of JEFT
10 th	1 st	4.4 Parameters of JFET & establish relation among JFET parameters
	2 nd	4.5 JEFT as an amplifier

	3 rd	4.5 Construction and working principle of MOSEFT 4.6 Classification of MOSEFT
11 th	1 st	4.7 Characteristics (Drain & Transfer) of MOSEFT
	2 nd	4.8 Explain the operation of CMOS, VMOS & LDMOS.
	3 rd	FEED BACK AMPLIFIER & OSCILLATOR 5.1 Define & classify Feedback Amplifier 5.2 Types of feedback – negative & positive feedback.
12 th	1 st	5.3 Characteristics voltage gain, bandwidth, input Impedance output impedance, stability, noise and distortion in amplifiers.
	2 nd	5.4 Oscillator 5.4.1 Block diagram of sine wave oscillator
	3 rd	5.4.2 Types Requirement of oscillation
13 th	1 st	5.4.3 Barkhausen criterion
	2 nd	5.5 LC oscillators 5.5.1 Colpitts Oscillators
	3 rd	5.5.2 Hartley Oscillators
14 th	1 st	5.5.3 Wien Bridge Oscillators
	2 nd	5.5.3 Wien Bridge Oscillators
	3 rd	5.5.3 Wien Bridge Oscillators
15 th	1 st	Integrated Circuit Fabrication Process 6.1 Oxidation 6.2 Diffusion 6.3 Ion implantation
	2 nd	6.4 Photo-lithography 6.5 Etching
	3 rd	6.7 Chemical vapor deposition 6.8 Sputtering 6.9 Twin-tub CMOS process

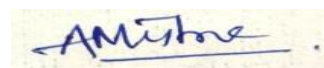
Course Outcomes (COs) - Electronics Devices

CO1: Describe the principles of semiconductor physics and apply it to electronic devices.

CO2: Select different devices for different applications.

CO3: Explain the mathematical models of semiconductor devices for circuits.

CO4: Discuss the basic processes required for fabrication of electronic devices.



Signature of Teaching Faculty

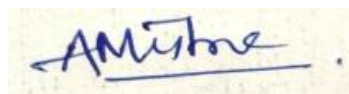
ACADEMIC LESSON PLAN OF WINTER 2026

Discipline: ETC	Semester: 3 rd	Name of the Teaching Faculty: ABINASH MISHRA
Subject: SIGNAL & SYSTEMS	No. of days/per week class allotted: 3p/week	Semester from: 01.07.2026 to 05.11.2026 No. of weeks: 15 weeks
Week	Class/week	Theory Topics
1 st	1 st	Introduction to Signals and Systems 1.1 Signals and systems as seen in everyday life
	2 nd	1.2 Signals and systems in various branches of engineering
	3 rd	1.3 Electrical, mechanical, hydraulic, thermal, biomedical signals and systems as examples
2 nd	1 st	1.3 Electrical, mechanical, hydraulic, thermal, biomedical signals and systems as examples
	2 nd	1.4 Extracting the common essence and requirements of signal and system
	3 rd	Formalizing signals 2.1 Energy and power signals
3 rd	1 st	2.2 Signal properties 2.2.1 Periodicity 2.2.2 Absolute integrability 2.2.3 Determinism and stochastic character
	2 nd	2.3 Some special signals of importance 2.3.1 The unit step 2.3.2 The unit impulse 2.3.3 The sinusoid 2.3.4 The complex exponential
	3 rd	2.4 some special time-limited signals 2.4.1 Continuous and discrete time signals, 2.4.2 Continuous and discrete amplitude signals.
4 th	1 st	2.5 Formalizing systems- system properties 2.5.1 Linearity 2.5.2 Additivity and homogeneity
	2 nd	2.5.3 Shift-invariance 2.5.4 Causality
	3 rd	2.5.5 Stability 2.5.6 Reliability
5 th	1 st	Continuous time and discrete time Systems 3.1 Linear shift-invariant (LSI) systems in detail
	2 nd	3.2 The impulse response and step response
	3 rd	3.3 Convolution
6 th	1 st	3.4 Input-output behavior with aperiodic convergent inputs
	2 nd	3.5 Cascade interconnections
	3 rd	3.6 Characterization of causality and stability of linear shift-invariant systems
7 th	1 st	3.7 System representation through differential equations and difference equations
	2 nd	Periodic and semi-periodic inputs to an LSI system 4.1 The notion of a frequency response and its relation to the impulse response
	3 rd	4.2 Fourier series representation
8 th	1 st	4.3 The Fourier Transform
	2 nd	4.4 Convolution/multiplication and their effect in the frequency domain, magnitude and phase response, Fourier domain duality.
	3 rd	4.5 The Discrete-Time Fourier Transform (DTFT)
9 th	1 st	4.6 The Discrete Fourier Transform (DFT)
	2 nd	4.7 Parseval's Theorem 4.8 The idea of signal space and Orthogonal bases of signals.

	3 rd	Laplace Transform for continuous time signals and systems- 5.1 The notion of Eigen functions of LSI systems
10 th	1 st	5.2 A basis of Eigen functions
	2 nd	5.3 Region of convergence
	3 rd	5.4 System functions
11 th	1 st	5.5 Poles and zeros of system functions and signals
	2 nd	5.6 Laplace domain analysis
	3 rd	5.7 Solution to differential equations and system behavior 5.8 Generalization of Parseval's Theorem
12 th	1 st	System realization 6.1 System realization through block-diagram representation and system interconnection
	2 nd	6.2 State-space analysis and multi-input, multi-output representation.
	3 rd	6.3 The state-transition matrix and its role.
13 th	1 st	6.4 The Sampling Theorem and its implications 6.4.1 Spectra of sampled signals.
	2 nd	6.5 Reconstruction: 6.5.1 Ideal interpolator 6.5.2 Zero-order hold 6.5.3 First-order hold
	3 rd	6.6 Aliasing and its effects.
14 th	1 st	6.7 Relation between continuous and discrete time systems.
	2 nd	Applications of signal and system theory 7.1 Modulation for communication and filtering
	3 rd	7.1 Modulation for communication and filtering
15 th	1 st	7.2 Time-frequency representation and the uncertainty principle
	2 nd	7.2 Time-frequency representation and the uncertainty principle
	3 rd	7.3 Short-time Fourier Transforms and wavelet transforms.

Course Outcomes (COs) - SIGNAL & SYSTEMS

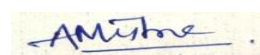
- CO1: Identify the sources of signals, and systems in real life.
- CO2: Characterize different types of signals and systems.
- CO3: Represent continuous-time and discrete-time systems in different mathematical forms.
- CO4: Analyze system behaviour using time and frequency domain techniques.



Signature of Teaching Faculty

ACADEMIC LESSON PLAN OF WINTER 2026

Discipline: ETC	Semester: 3 rd	Name of the Teaching Faculty: ABINASH MISHRA
Subject: ELECTRONICS DEVICE LAB	No. of days/per week class allotted: 4p/week	Semester from: 01.07.2026 to 05.11.2026 No. of weeks: 15 weeks
Week	Class/week	Theory Topics
1 st	1 st	Characteristics of P-N junction diode
	2 nd	Characteristics of P-N junction diode
2 nd	1 st	Characteristics of Zener diode
	2 nd	Characteristics of Zener diode
3 rd	1 st	Input-Output and Transfer characteristics of CE and CC Amplifier
	2 nd	Input-Output and Transfer characteristics of CE and CC Amplifier
4 th	1 st	Characteristics of Photo-diode and Photo transistor
	2 nd	Characteristics of Photo-diode and Photo transistor
5 th	1 st	Transfer characteristics of JFET
	2 nd	Transfer characteristics of JFET
6 th	1 st	Transfer characteristics of MOSFET (with depletion and enhancement mode)
	2 nd	Transfer characteristics of MOSFET (with depletion and enhancement mode)
7 th	1 st	Characteristics of LED with three different wavelengths
	2 nd	Characteristics of LED with three different wavelengths
8 th	1 st	Half wave rectifier
	2 nd	Half wave rectifier
9 th	1 st	Full wave rectifier with 2 diodes.
	2 nd	Full wave rectifier with 2 diodes.
10 th	1 st	Full wave rectifier with 4 diodes (Bridge rectifier)
	2 nd	Full wave rectifier with 4 diodes (Bridge rectifier)
11 th	1 st	Construct & Find the gain of (i) Class A Amplifier (ii) Class B Amplifier (iii) Class C Tuned Amplifier
	2 nd	Construct & Find the gain of (i) Class A Amplifier (ii) Class B Amplifier (iii) Class C Tuned Amplifier
12 th	1 st	Construct & test Class B -Push Pull amplifier & observe the wave form
	2 nd	Construct & test Class B -Push Pull amplifier & observe the wave form
13 th	1 st	Construct & calculate the frequency & Draw the wave form of (i) Hartly Oscillator (ii) Collpit's Oscillator (iii) Wein Bridge Oscillator (iv) R-C phase shift Oscillator
	2 nd	Construct & calculate the frequency & Draw the wave form of (i) Hartly Oscillator (ii) Collpit's Oscillator (iii) Wein Bridge Oscillator (iv) R-C phase shift Oscillator
14 th	1 st	Simulation experiments using PSPICE or Multisim.
	2 nd	Simulation experiments using PSPICE or Multisim.
15 th	1 st	Simulation experiments using PSPICE or Multisim.
	2 nd	Simulation experiments using PSPICE or Multisim.



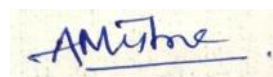
Signature of Teaching Faculty

Course Outcomes (COs)

Course Code: **ETCPC211**

Course Title: **Electronics Devices Laboratory**

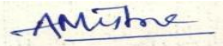
CO No.	Course Outcome
CO1	Perform experiments to obtain and analyze the characteristics of semiconductor devices such as P-N junction diode, Zener diode, BJT, JFET, MOSFET, LED, photodiode and phototransistor.
CO2	Construct, test and analyze rectifier circuits, amplifier circuits and oscillator circuits using semiconductor devices.
CO3	Measure and interpret important electronic parameters such as gain, frequency response, transfer characteristics and output waveforms using standard laboratory instruments.
CO4	Simulate semiconductor devices and analog electronic circuits using PSPICE/Multisim and compare simulation results with practical observations.
CO5	Identify electronic components, write their specifications, prepare laboratory reports and demonstrate safe laboratory practices with effective teamwork and communication.



Signature of Teaching Faculty

ACADEMIC LESSON PLAN OF WINTER 2026

Discipline: ETC	Semester: 5 th	Name of the Teaching Faculty: ABINASH MISHRA
Subject: PR:1- DATA COMMUNICATION & COMPUTER NETWORKS LABORATORY	No. of days/per week class allotted: 4p/week	Semester from: 01.07.2026 to 05.11.2026 No. of weeks: 15 weeks
Week	Class/week	Theory Topics
1 st	1 st	Recognize the physical topology and cabling (coaxial, OFC, UTP, STP) of a network
	2 nd	Recognize the physical topology and cabling (coaxial, OFC, UTP, STP) of a network
2 nd	1 st	Recognition and use of various types of connectors RJ-45, RJ-11, BNC and SCST
	2 nd	Recognition and use of various types of connectors RJ-45, RJ-11, BNC and SCST
3 rd	1 st	Making of cross cable and straight cable
	2 nd	Making of cross cable and straight cable
4 th	1 st	Install and configure a network interface card in a workstation
	2 nd	Install and configure a network interface card in a workstation
5 th	1 st	Identify the IP address of a workstation and the class of the address and configure the IP Address on a workstation
	2 nd	Identify the IP address of a workstation and the class of the address and configure the IP Address on a workstation
6 th	1 st	Managing user accounts in windows and LINUX
	2 nd	Managing user accounts in windows and LINUX
7 th	1 st	Sharing of Hardware resources in the network.
	2 nd	Sharing of Hardware resources in the network.
8 th	1 st	Use of Netstat and its options.
	2 nd	Use of Netstat and its options.
9 th	1 st	Connectivity troubleshooting using PING, IPCONFIG
	2 nd	Connectivity troubleshooting using PING, IPCONFIG
10 th	1 st	Installation of Network Operating System(NOS)
	2 nd	Installation of Network Operating System(NOS)
11 th	1 st	Create a network of at least 6 computers
	2 nd	Create a network of at least 6 computers
12 th	1 st	Study of Layers of Network and Configuring Network Operating System
	2 nd	Study of Layers of Network and Configuring Network Operating System
13 th	1 st	Study of Routing and Switching, configuring of Switch and Routers, troubleshooting of networks
	2 nd	Study of Routing and Switching, configuring of Switch and Routers, troubleshooting of networks
14 th	1 st	Study of Scaling of Networks, Design verities of LAN and forward of Traffic
	2 nd	Study of Scaling of Networks, Design verities of LAN and forward of Traffic
15 th	1 st	Study WAN concepts and Configure and forward Traffic in WAN
	2 nd	Study WAN concepts and Configure and forward Traffic in WAN



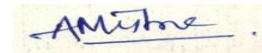
Signature of Teaching Faculty

Course Outcomes (COs)

Course Code: ETCPC305

Course Title: Data Communication & Computer Networks Laboratory

CO No.	Course Outcome
C01	Identify network topologies, transmission media, connectors, and prepare straight-through and crossover Ethernet cables following networking standards.
C02	Install and configure network interface cards, assign IPv4/IPv6 addresses, manage user accounts, and configure network operating systems in Windows and Linux environments.
C03	Design, implement, and verify small Local Area Networks (LANs) using switches and routers while configuring routing and switching functions.
C04	Apply network diagnostic and troubleshooting tools such as PING , IPCONFIG , and NETSTAT to identify and resolve connectivity issues in computer networks.
C05	Configure and analyze LAN and WAN communication, implement basic network programming concepts, and evaluate network performance, scalability, security, and traffic forwarding.



Signature of Teaching Faculty